



PATENT APPLICATION
Docket No. 4591-306
Client Ref. No. SPX200212-0009US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of: Jae-Yoon Sim
Serial No. 10/611,788 Group No. 2818
Filed: June 30, 2003 Examiner: Michael T. Tran
Confirmation No. 1324
Title: SEMICONDUCTOR MEMORY DEVICE WITH OFFSET-
COMPENSATED SENSING SCHEME

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Reasons for Allowance section of the latest office Action, the Examiner recited portions from the independent claims and stated that the prior art did not show the recited elements, in addition to the other elements of the claims, as a whole. The applicants note that it is a well known tenet of patent law that patent claims are read as a whole, rather than element by element. Therefore, the patentability of the claims is not necessarily based on only the claim elements that the Examiner noted, but rather, to the language of the claims themselves, in their entirety.

Customer No. 20575

Respectfully submitted,

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Date: September 24, 2004


Kevin S. Ross



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TRANSMITTAL LETTER

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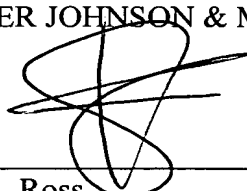
Enclosed for filing in the above-referenced application are the following:

- ☒ Publication and Issue Fee
- ☒ Applicant's Comments on Examiner's Statement of Reasons for Allowance
- ☒ In connection with issuance of a patent:
 - ☐ Supplemental Declaration ☒ PTO Form 85B
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

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